

FIG.1

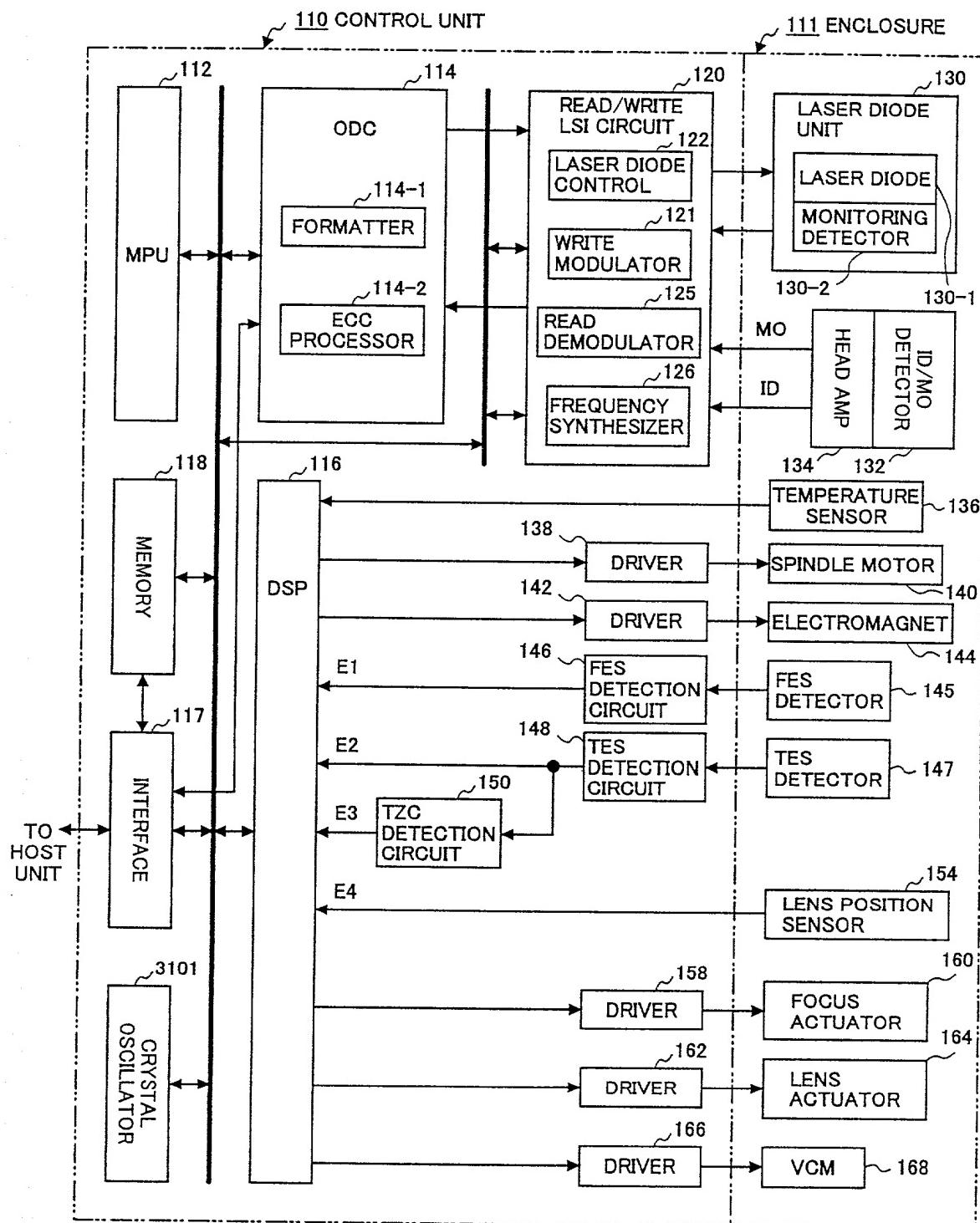


FIG. 2

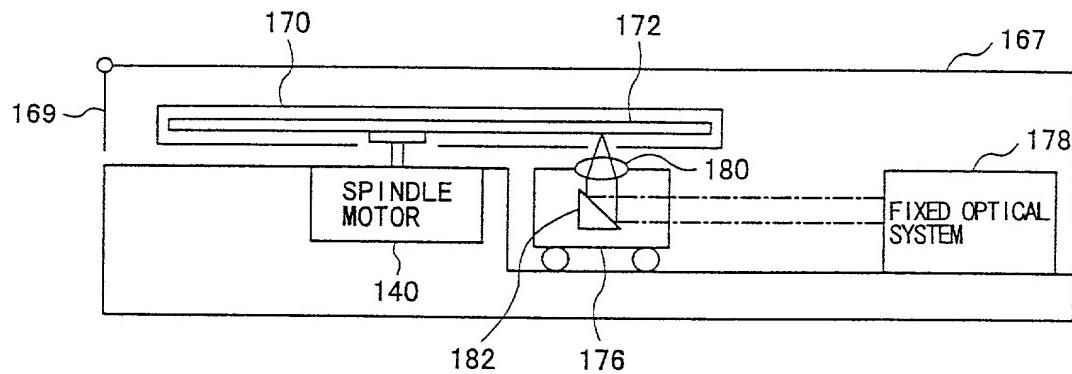


FIG.3

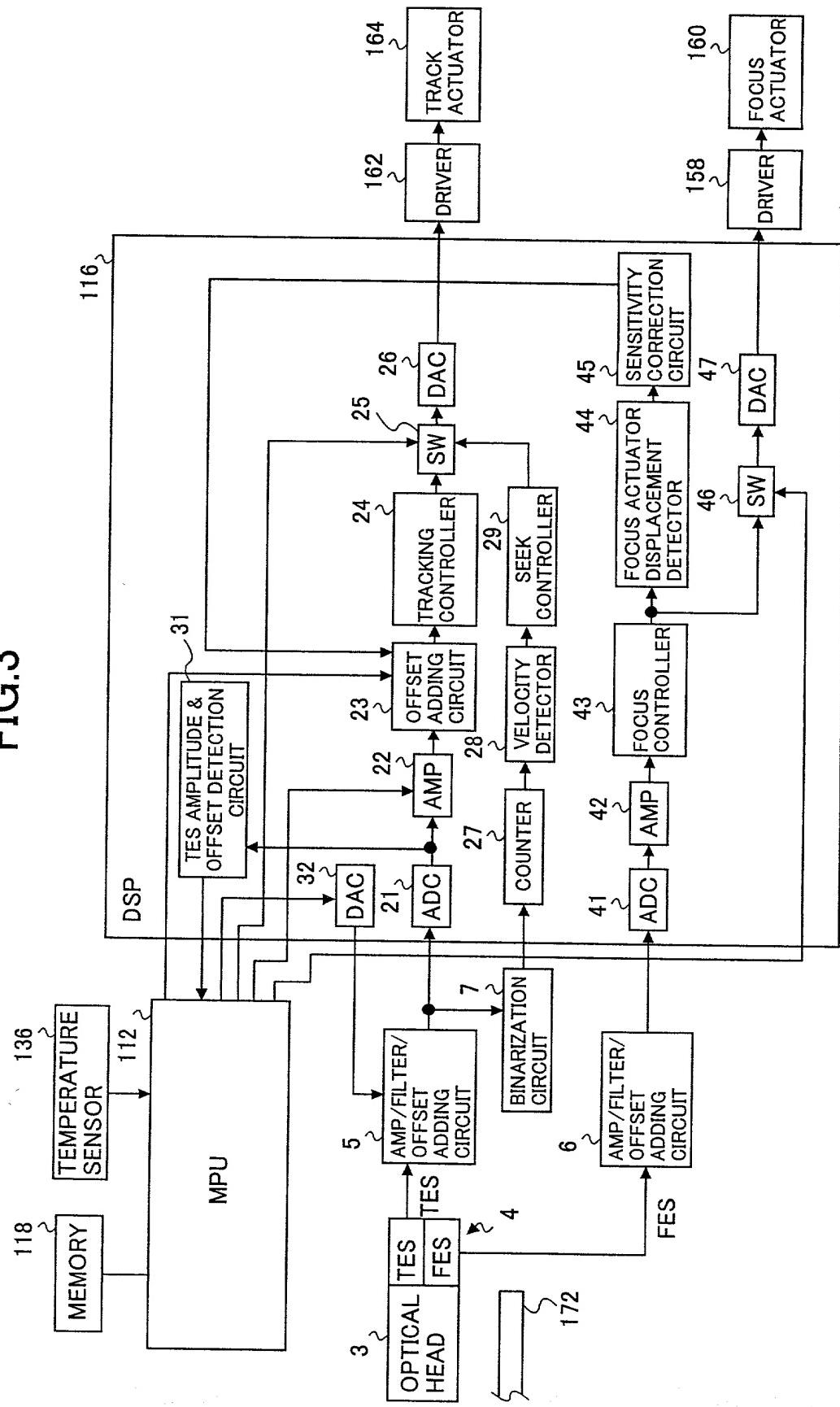
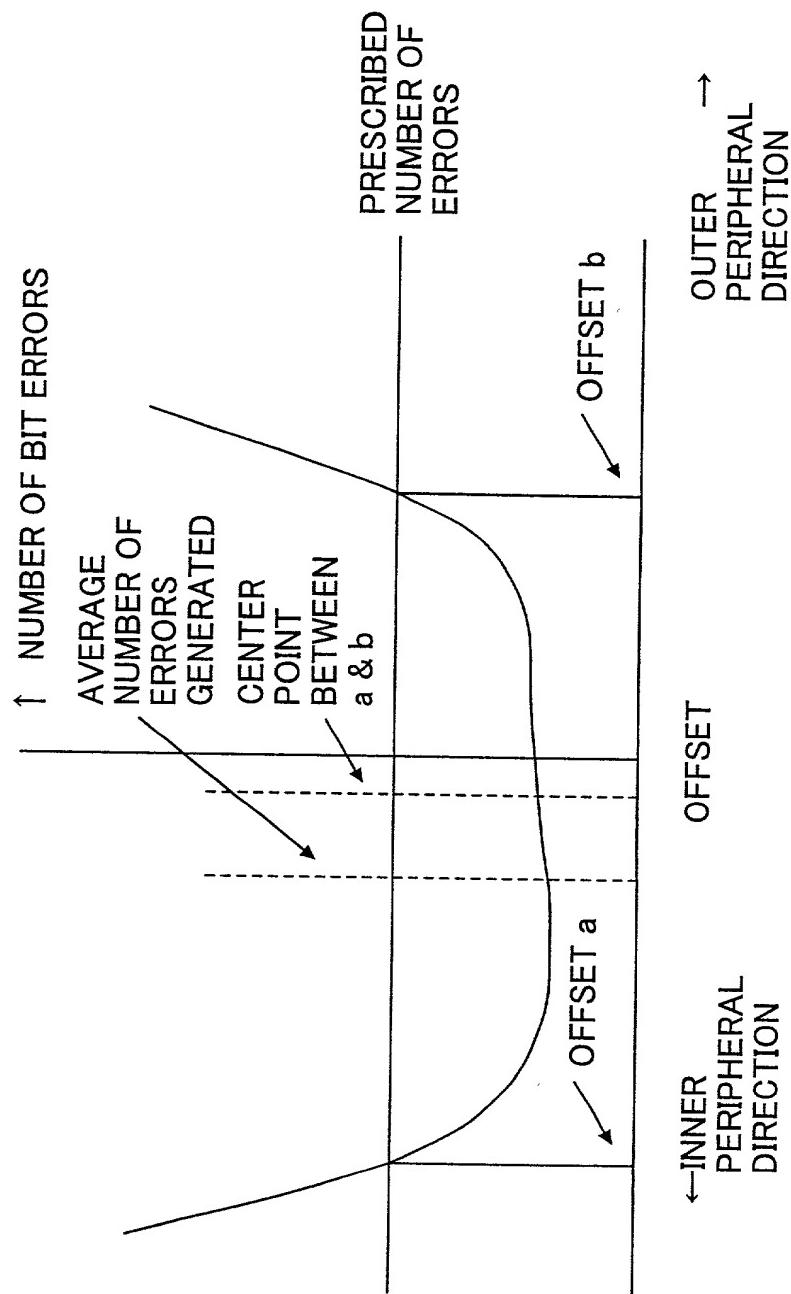


FIG.4



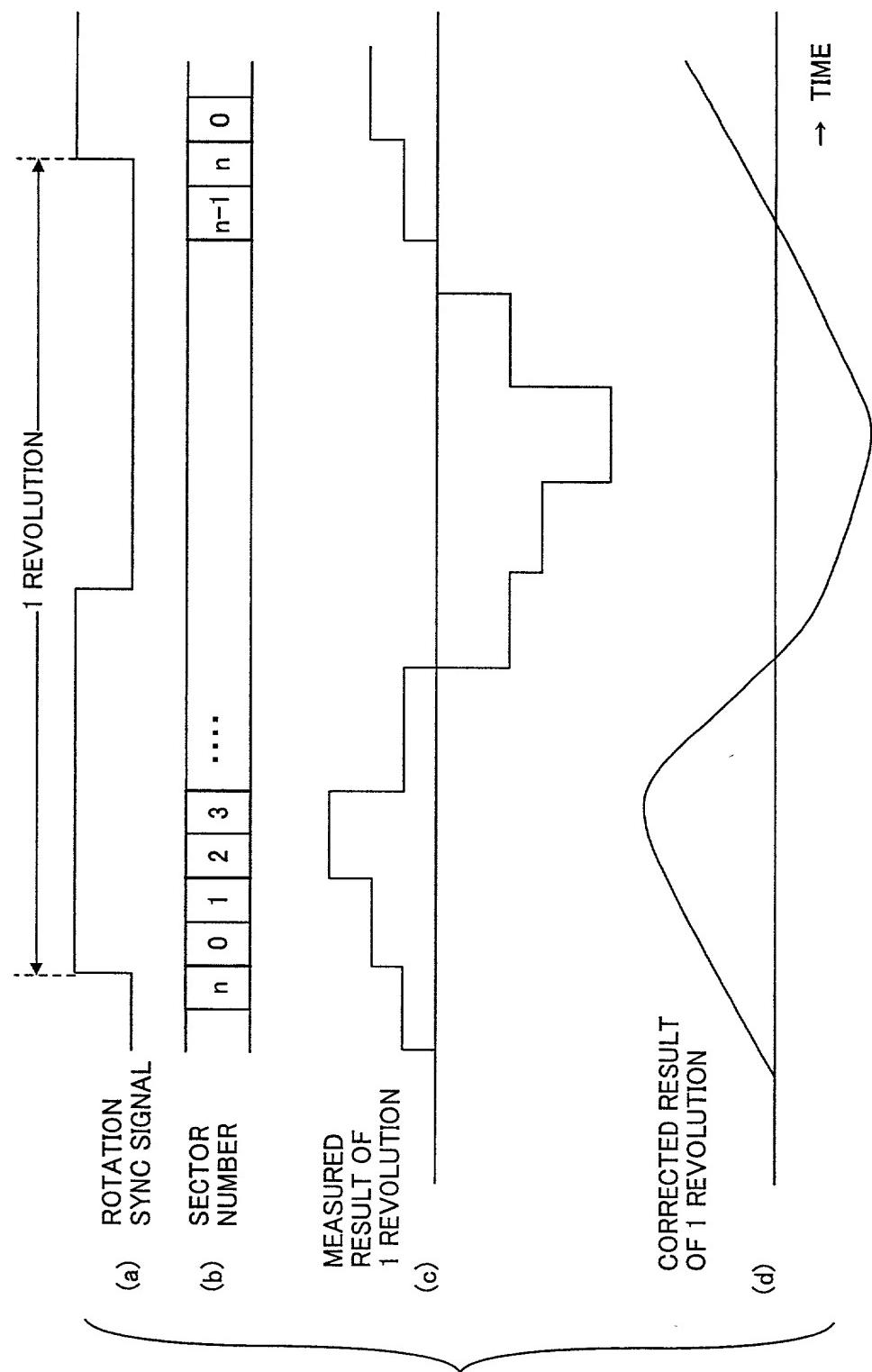


FIG.5

FIG.6A

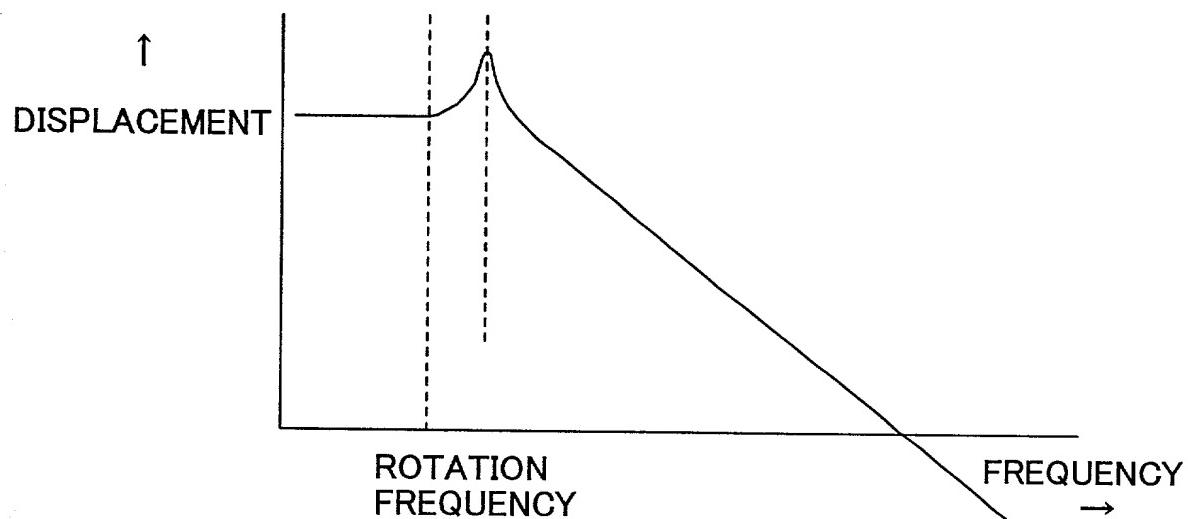
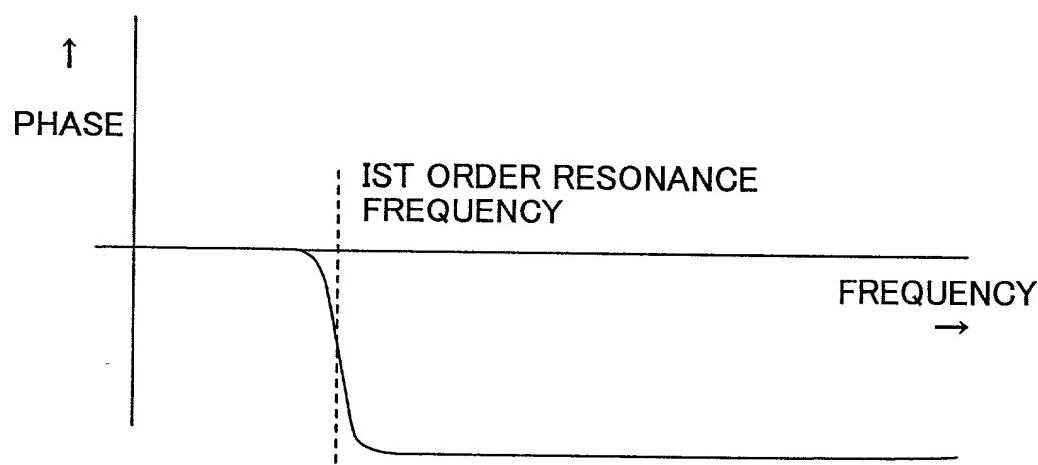


FIG.6B



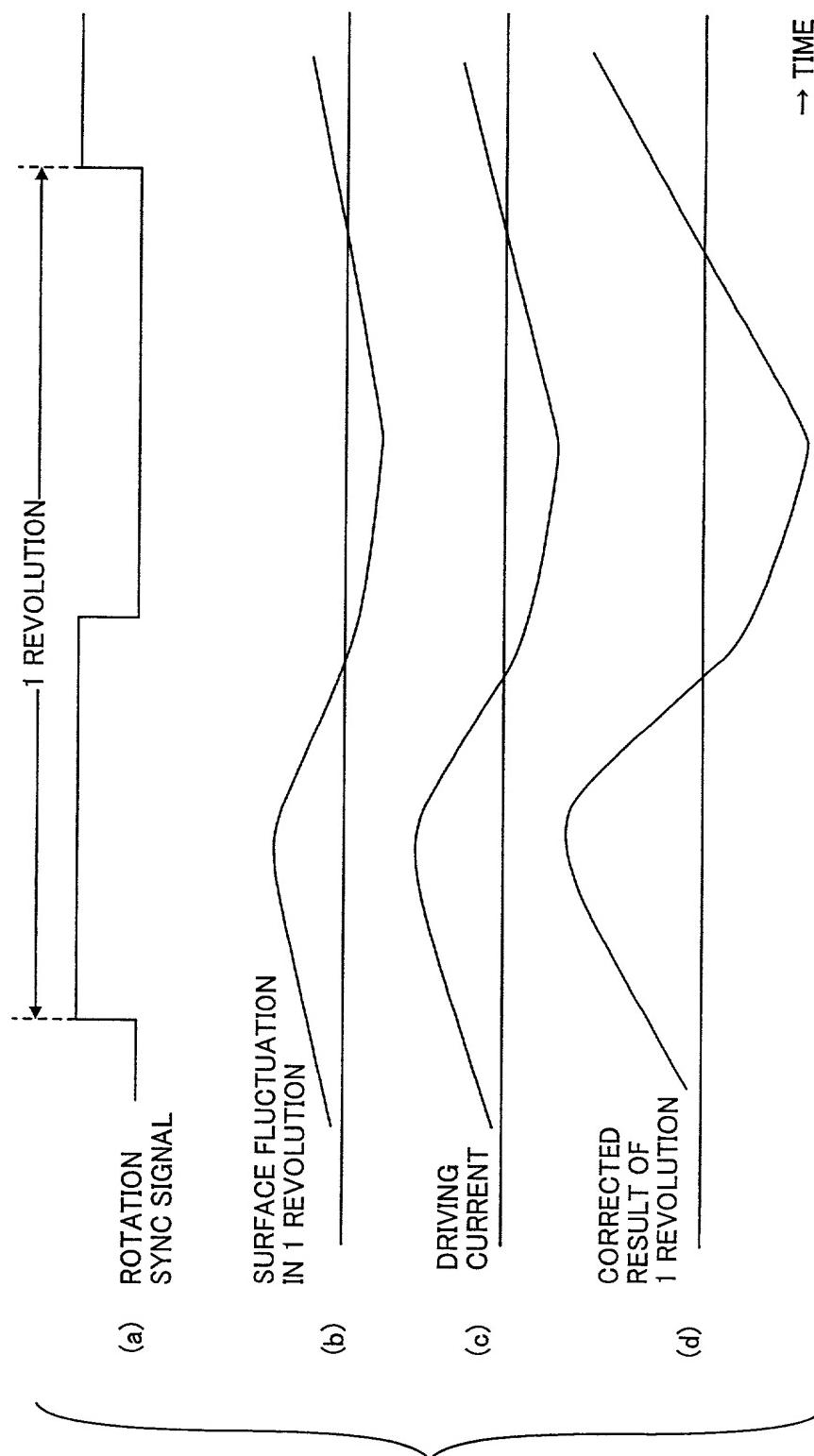


FIG. 7

FIG.8

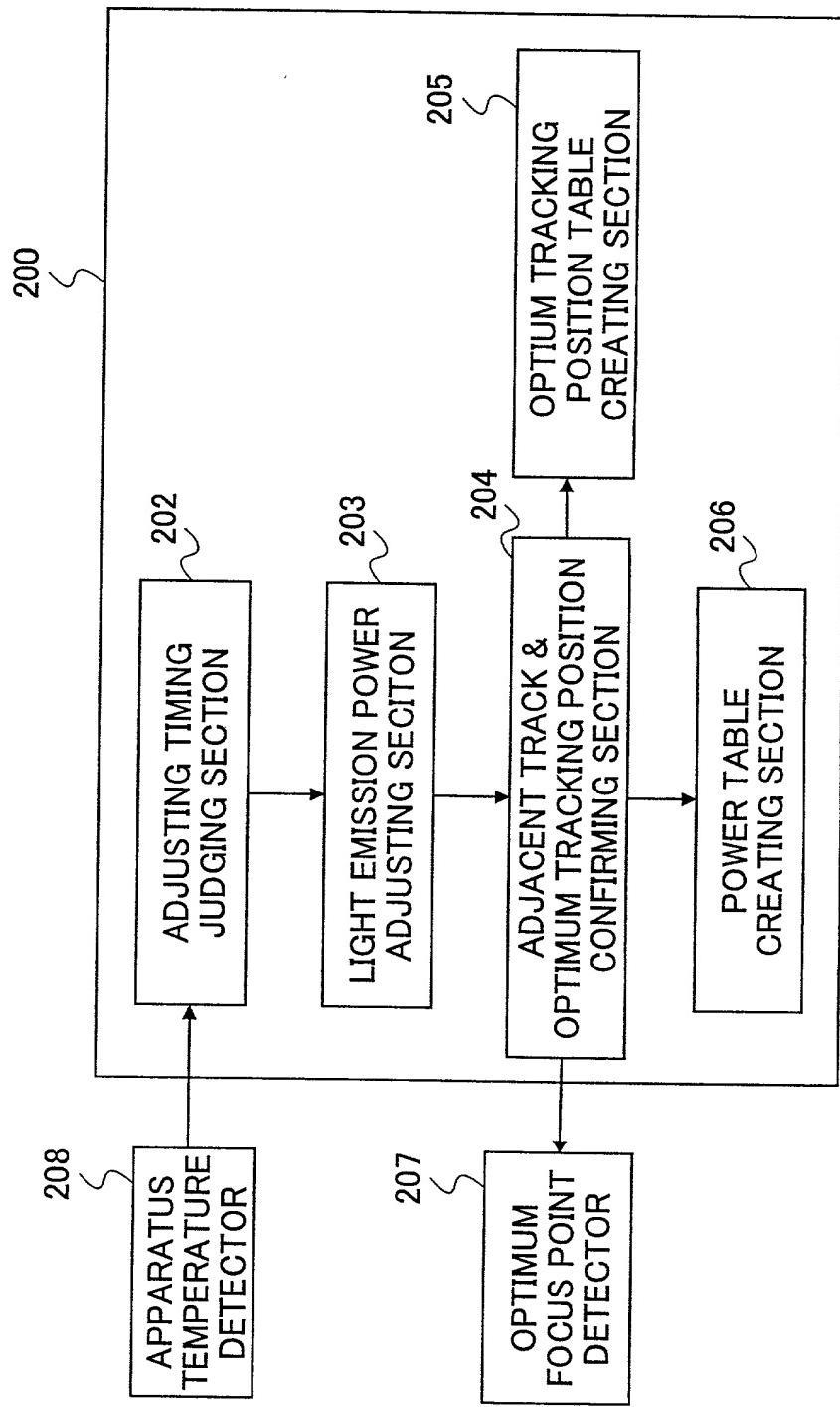


FIG.9

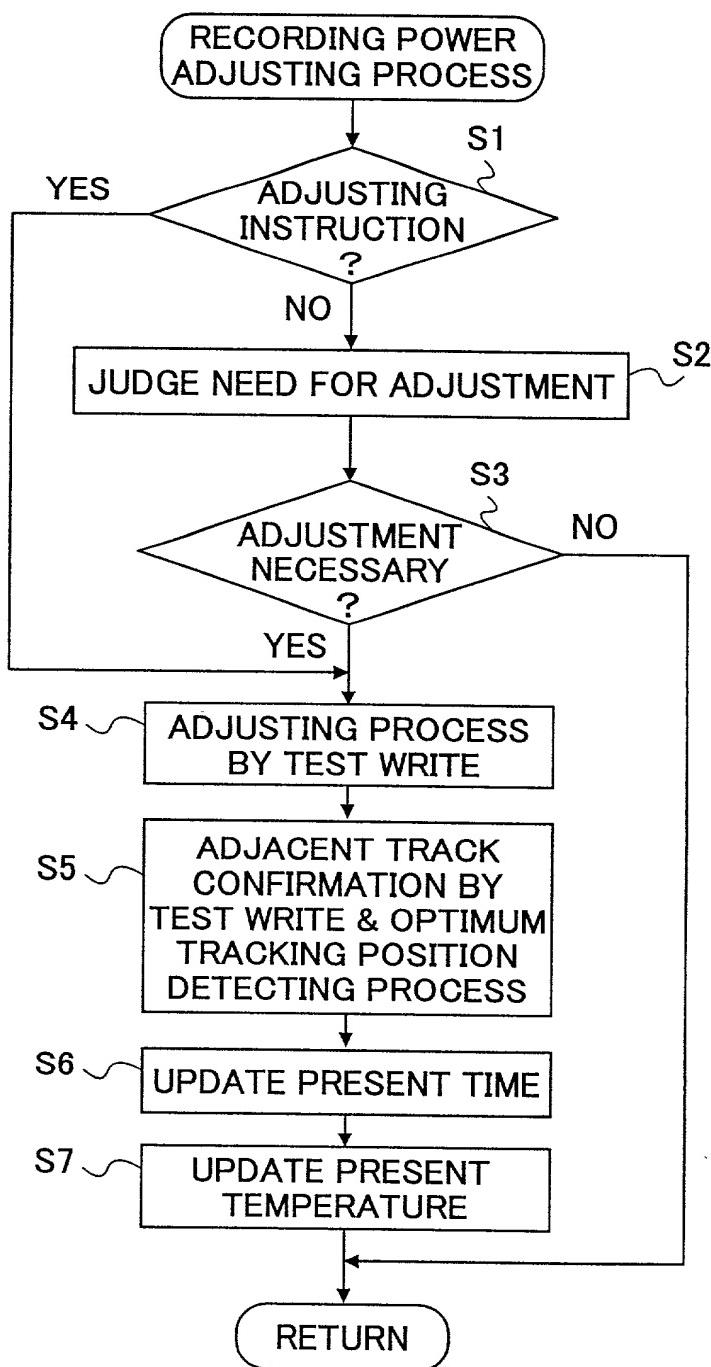


FIG.10

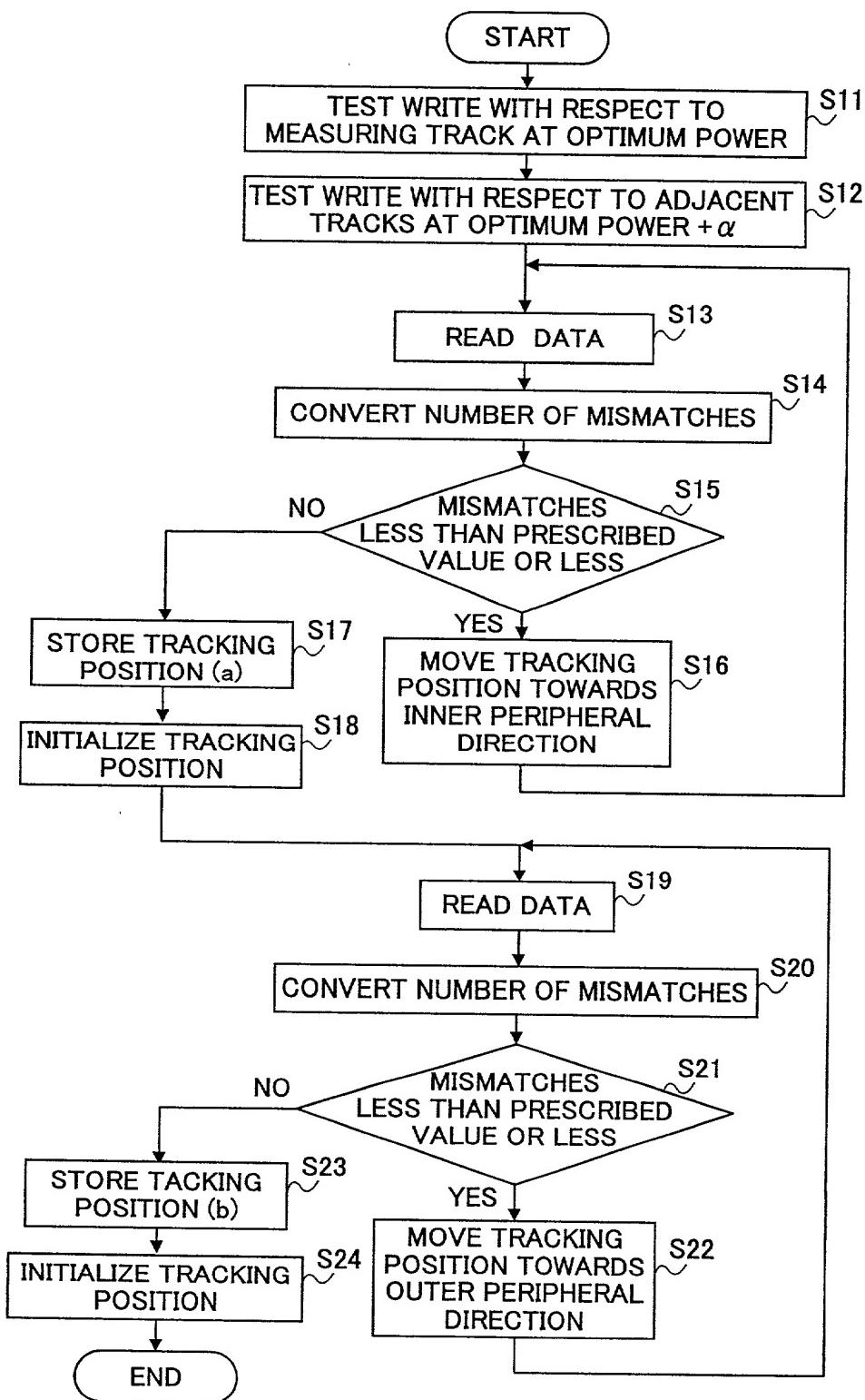
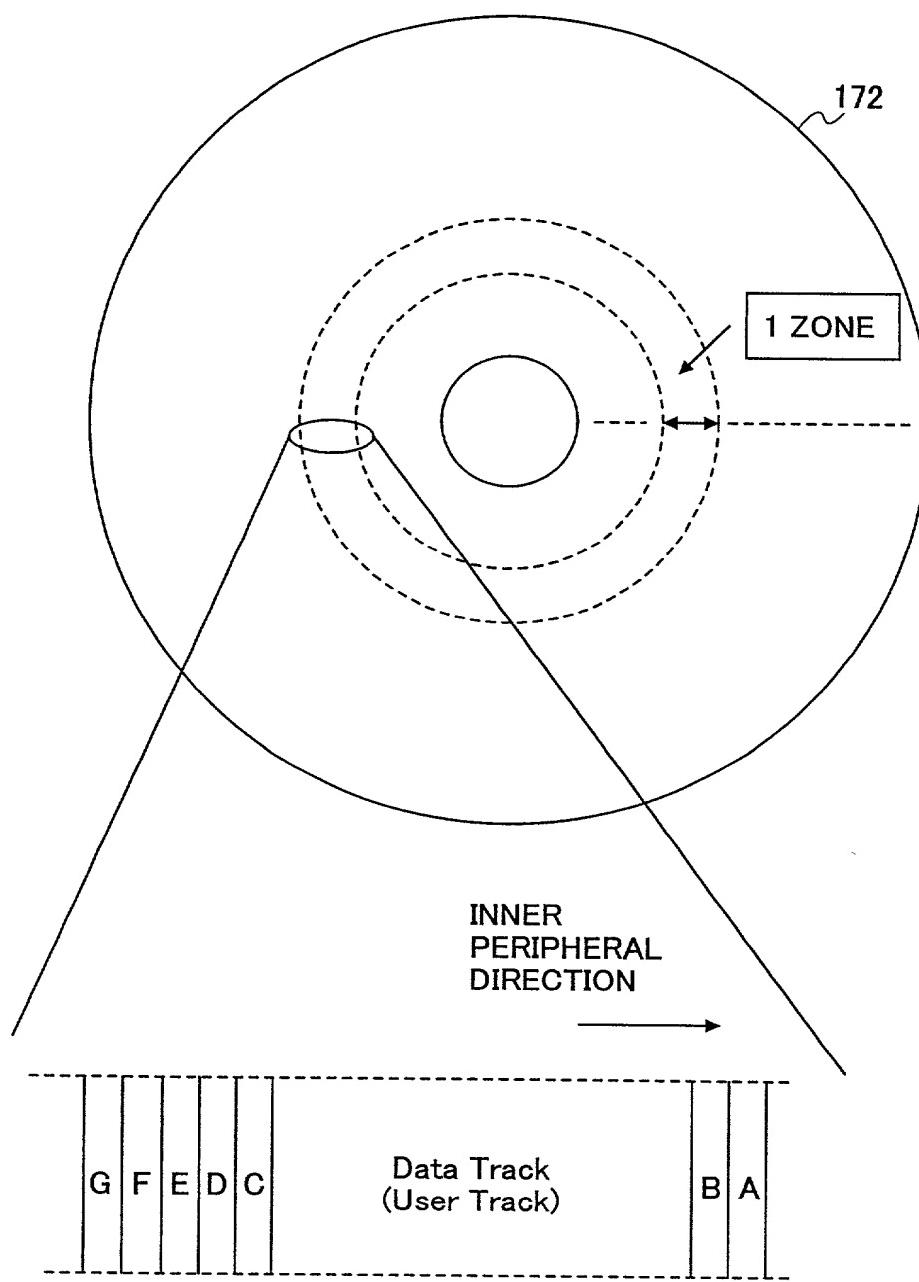


FIG.11



- A: Buffer 0
- B: Buffer 1
- C: Spare
- D: Buffer 2
- E: Test
- F: Buffer 3
- G: Buffer 4

FIG.12

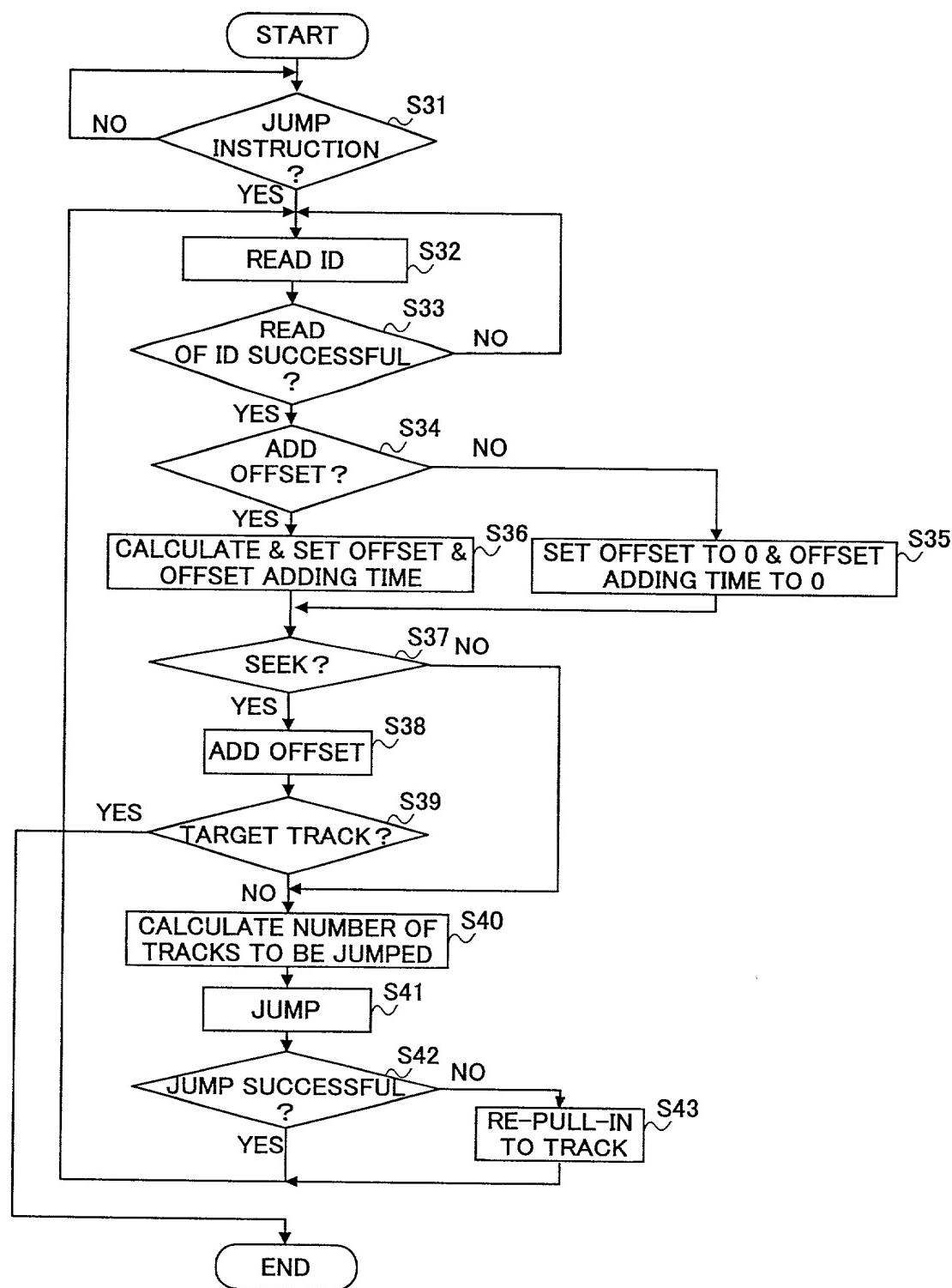


FIG. 13

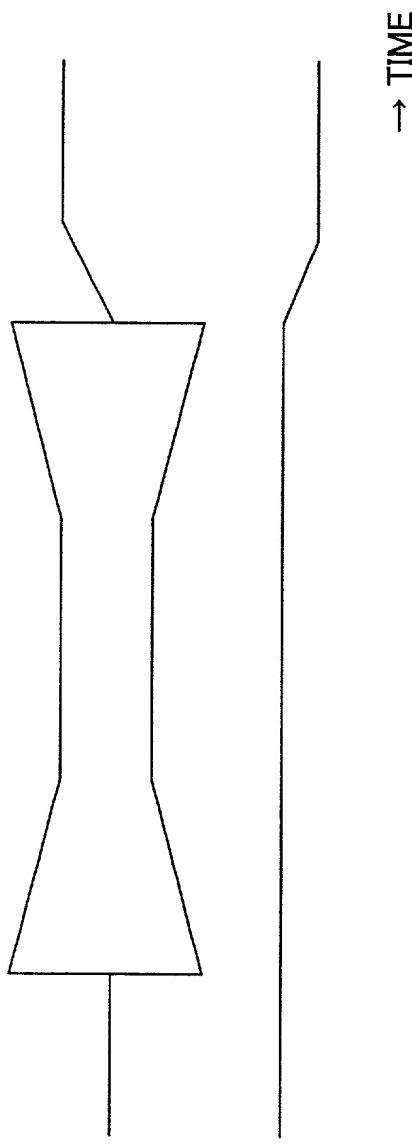
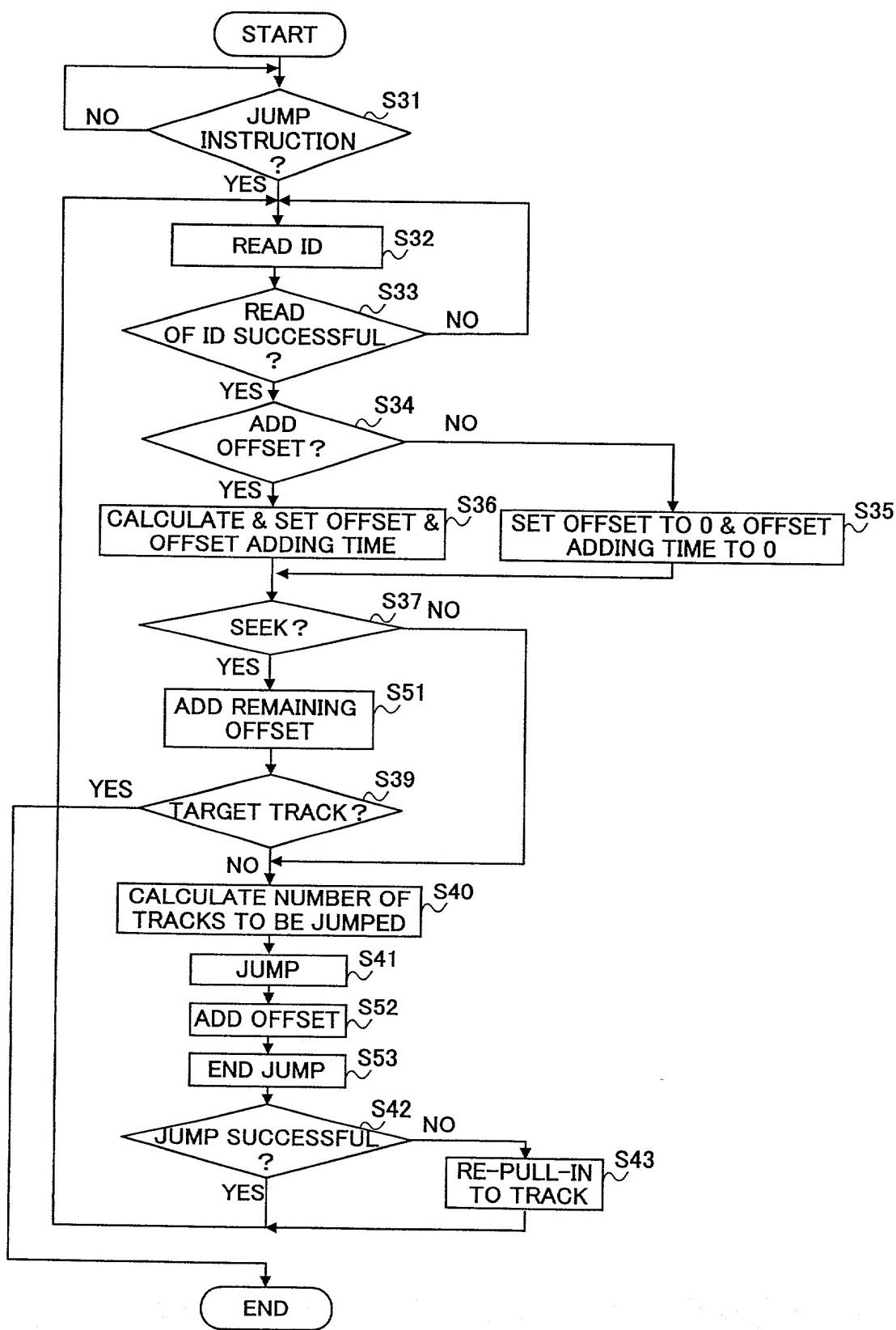


FIG.14



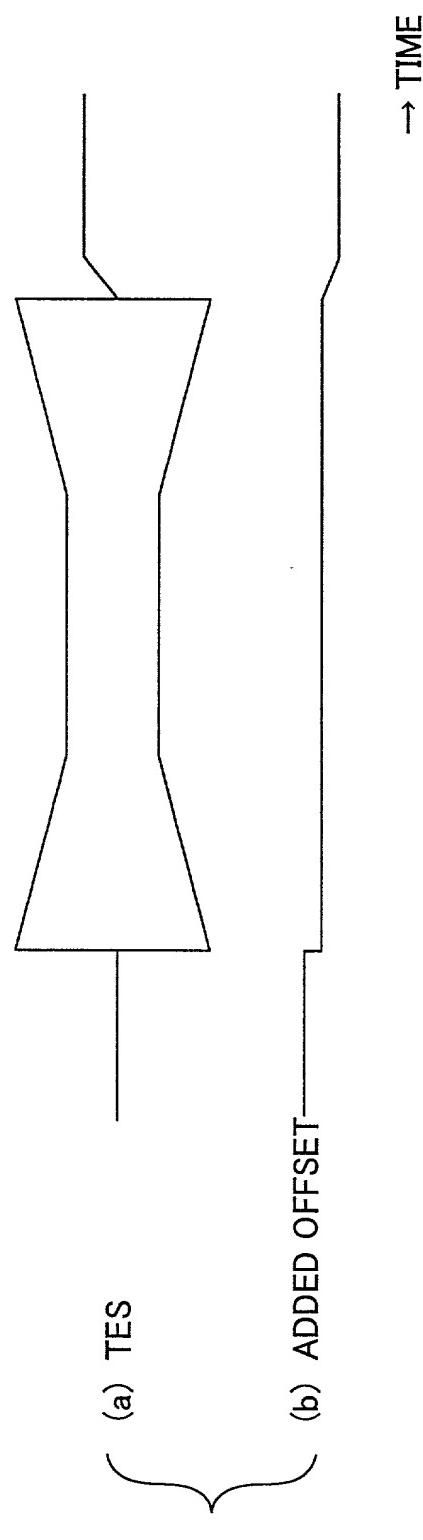


FIG. 15

FIG.16

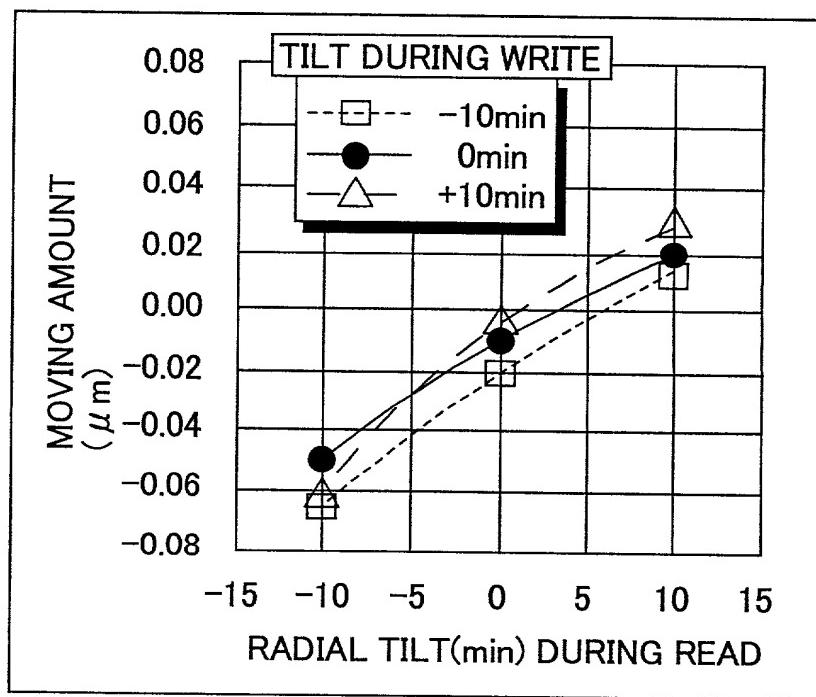


FIG.17

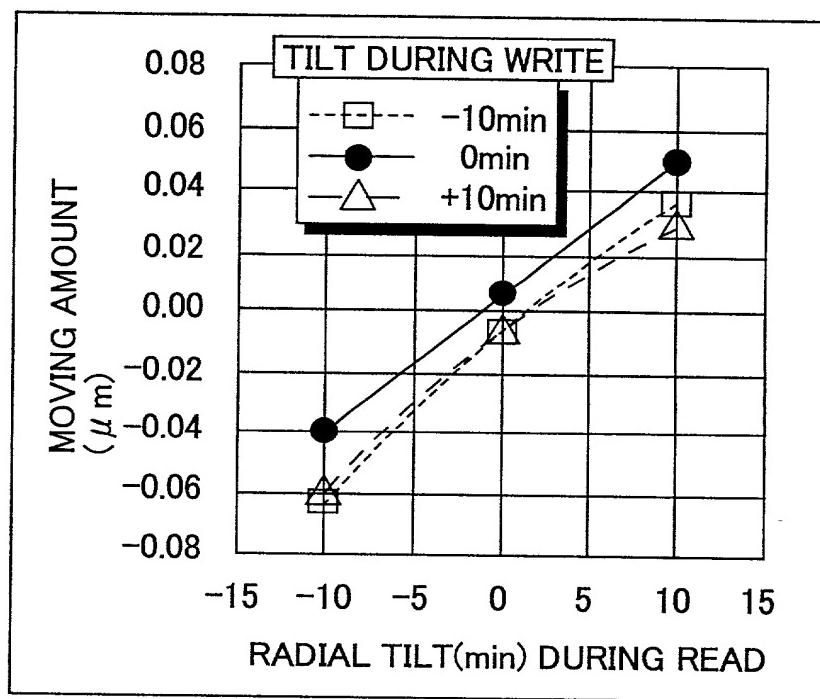


FIG. 18A

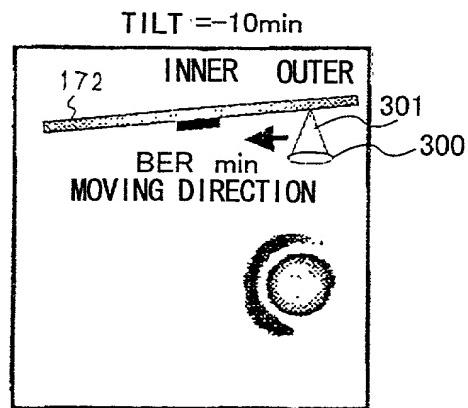


FIG. 18B

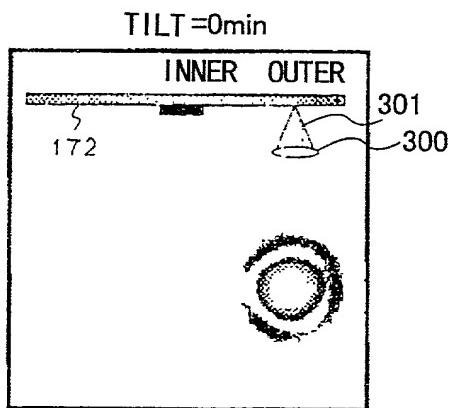


FIG. 18C

